UTC UNISONIC TECHNOLOGIES CO., LTD

UTT150N03

Preliminary

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

DESCRIPTION

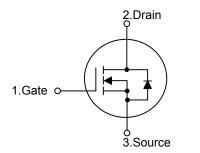
The UTC **UTT150N03** is a N-channel power MOSFET, using UTC's advanced trench technology to provide customers with a minimum on-state resistance, low gate charge and superior switching performance.

The UTC **UTT150N03** is generally applied in DC to DC convertor, synchronous or conventional switching PWM controllers.

FEATURES

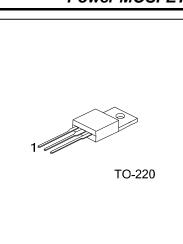
- * 150A, 30V, $R_{DS(ON)}$ =4.1m Ω @ V_{GS}=10V, I_D = 75A
- $R_{DS(ON)}$ =4.6m Ω @ V_{GS}=4.5V, I_D = 75A
- * High Switching Speed
- * High Power and Current Handling Capability
- * RoHS Compliant

SYMBOL



ORDERING INFORMATION

Ordering Number		Daakaga		Pin Assignment			Decking	
Lead Free	Halogen Free	Package		1	2	3	Packing	
UTT150N03L-TA3-T	UTT150N03G-TA3-T	TO-2	220	G	D	S	Tube	
Note: Pin Assignment: G: Gate D: Drain S: Source								
UTT150N03L-TA3-T (1)Packing Type (2)Package Type (3)Lead Free				ube 3: TO-220 Halogen Fre	ee, L: Leac	l Free		



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Power MOSFET

■ **ABSOLUTE MAXIMUM RATINGS** (T_c=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V _{DSS}	30	V	
Gate-Source Voltage		V _{GSS}	±20	V	
Drain Current	Continuous	I _D	150	А	
	Pulsed	I _{DM}	266	А	
Single Pulsed Avalanche Energy (Note 2)		E _{AS}	300	mJ	
Power Dissipation	Power Dissipation		160	W	
	Derate above 25°C	- P _D -	1.07	W/°C	
Junction Temperature		TJ	+150	°C	
Storage Temperature		T _{STG}	-55~+150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	62	°C/W	
Junction to Case	θ _{JC}	0.94	°C/W	



■ ELECTRICAL CHARACTERISTICS (T_c=25°C, unless otherwise noted)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μΑ, V _{GS} =0V	30			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =24V, V _{GS} =0V			1	μA
Gate- Source Leakage	Forward		V _{GS} =+20V, V _{DS} =0V			+100	nA
Current Reve		I _{GSS}	V _{GS} =-20V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250µA	1		3	V
Static Drain-Source On-State		R _{DS(ON)}	V _{GS} =10V, I _D =75A	3.4 4		4.1	
Resistance			V _{GS} =4.5V, I _D =75A		4.0	4.6	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		CISS			5200		pF
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =15V, f=1.0MHz		970		рF
Reverse Transfer Capacitance		C _{RSS}			570		рF
SWITCHING PARAMETERS							
Gate Resistance		R_G	V _{GS} =0.5V, f=1MHz		2.1		Ω
Total Gate Charge		$Q_{G(TOT)}$	V _{GS} =0~10V, V _{DD} =15V, I _D =75A, I _G =1mA		106	132	nC
		Q _{G(5)}	V _{GS} =0~5V, V _{DD} =15V, I _D =75A, I _G =1mA		56	69	nC
Threshold Gate Charge		Q _{G(TH)}	V _{GS} =0~1V, V _{DD} =15V, I _D =75A, I _G =1mA		5.0	6.5	nC
Gate to Source Charge		Q _{GS}			15		nC
Gate Charge Threshold to Plateau		Q _{GS2}	V _{DD} =15V, I _D =75A, I _G =1mA		10		nC
Gate to Drain Charge		Q_{GD}	_		23		nC
Turn-ON Time		t _{ON}				168	ns
Turn-ON Delay Time		t _{D(ON)}	_		11		ns
Rise Time		t _R	V _{DD} =15V, I _D =75A, V _{GS} =4.5V,		105		ns
Turn-OFF Delay Time		t _{D(OFF)}	R _{GS} =3.3Ω		70		ns
Fall-Time		t _F			46		ns
Turn-OFF Time		t _{OFF}				173	ns
SOURCE- DRAIN DIODE RAT	INGS AN	D CHARACT	ERISTICS				
Drain-Source Diode Forward Voltage		V _{SD}	I _S =150A			1.25	V
			I _S =15A			1.0	V
Body Diode Reverse Recovery Time		t _{RR}	−I _{SD} =150A, dI _{SD} /dt=100A/µs			37	ns
Body Diode Reverse Recovery Charge		Q _{RR}	I_{SD} = 130A, U_{SD}/U_{C} = 100A/µS			21	nC

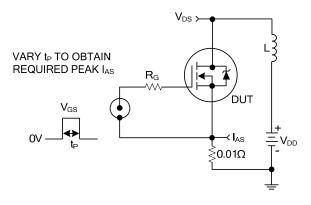
Notes: 1. Package current limitation is 80A.

2. Starting $T_{\rm J}$ = 25°C, L = 0.15mH, $I_{\rm AS}$ = 64A, $V_{\rm DD}$ = 27V, $V_{\rm GS}$ =10V

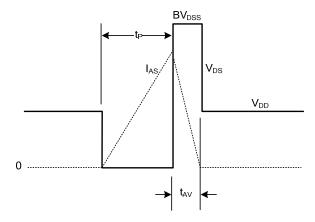
3. Pulse Test: Pulse width \leq 300µs, Duty cycle \leq 2%

UTT150N03

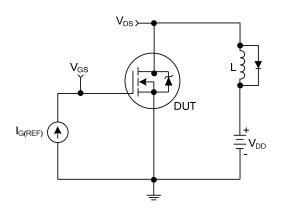
TEST CIRCUITS AND WAVEFORMS



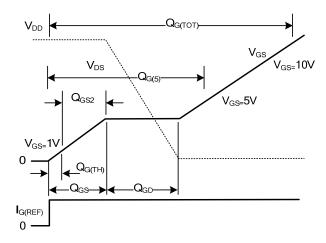
Unclamped Energy Test Circuit



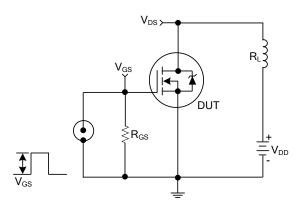
Unclamped Energy Waveforms



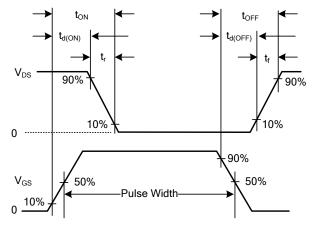




Gate Charge Waveforms



Switching Time Test Circuit



Switching Time Waveforms



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